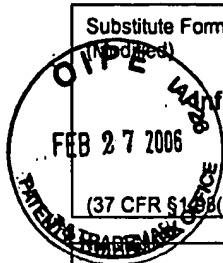


Substitute Form PTO-1449 (Updated)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-618001	Application No. 10/057,738
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR § 1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date January 25, 2002	Group Art Unit 2189

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
S.G.	AA	4,189,767	02/1980	Ahuja			
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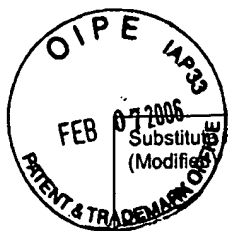
Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
S.G.	AL	1 191 445	03/2002	Europe				
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Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
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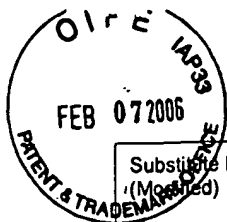
Examiner Signature <i>Sharon Guy</i>	Date Considered 4/6/2006
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	



Form PTO-1449	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-618001	Application No. 10/057,738
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date January 25, 2002	Group Art Unit 2187

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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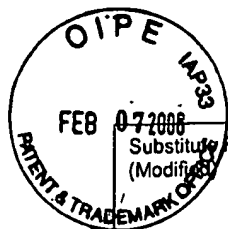
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-618001	Application No. 10/057,738
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date January 25, 2002	Group Art Unit 2187

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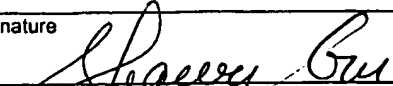
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	ALLL	Chang, Shu-Ping et al., "An Overview of the Pipelined Common Buffer Architecture (PCBA) for Memory Based Packet/Cell Switching Systems", <i>IEEE</i> , pp. 288-297 (1994).
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	ANNN	Lin, Yu-Sheng et al., "Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches", <i>IEEE</i> , pp. 688-695 (1996).

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